

Amendments to the Claims

This listing of claims will replace all prior versions, and listings, of claims in the application.

Listing of Claims:

1. (Previously presented) A repeat circuit for use with an audio receive and reproduce device that has a normal mode and a replay mode, the repeat circuit including:

a RAM having an input connected to a receiving input and an output connected to a replay audio input of said audio receive and reproduce device; and

a control adapted to switch said audio receive and reproduce device into replay mode by inhibiting application of incoming audio inputs to said device and for instead applying at least a portion of audio inputs stored in said RAM as audio inputs to said device via the replay audio input of said device, the audio reproduced by said device being selectively delayed from incoming audio inputs by a time dependent on where in said RAM said control begins the applying of audio inputs to said device;

wherein said device is configured to automatically control the rate at which said RAM is read out to said replay audio input of said device, said RAM being read out to said replay audio input of said device at a replay rate that is different than an incoming rate at which the incoming audio inputs are received to be stored in said RAM;

wherein said replay rate is selected so as to gradually eliminate the delay time without substantially impairing audio quality of the replayed audio, thereby automatically returning said device to normal mode.

2. (Previously presented) A circuit as claimed in claim 36, wherein the location in said RAM at which the applying of audio inputs begins, and thus the delay between incoming audio inputs and reproductions, is controllable in response to selective operation of said component.

3. (Original) A circuit as claimed in claim 2, wherein said delay is a function of at least one of the number of times said component is operated and the time said component is operated.
4. (Original) A circuit as claimed in claim 2, wherein when said device is receiving inputs from said RAM, the circuit is in replay mode, and including an output element providing a selected indication that said circuit is in said replay mode, said output element also providing an indication of said delay.
5. (Currently amended) A circuit as claimed in claim 4, repeat circuit for use with an audio receive and reproduce device that has a normal mode and a replay mode, the repeat circuit including:
- a RAM having an input connected to a receiving input and an output connected to a replay audio input of said audio receive and reproduce device;
 - a manually operable input component, and wherein said control is operable in response to a selected input from said input component, to switch said audio receive and reproduce device into replay mode; and
 - a control adapted to switch said audio receive and reproduce device into replay mode by inhibiting application of incoming audio inputs to said device and for instead applying at least a portion of audio inputs stored in said RAM as audio inputs to said device via the replay audio input of said device, the audio reproduced by said device being selectively delayed from incoming audio inputs by a time dependent on where in said RAM said control begins the applying of audio inputs to said device;
 - wherein said device is configured to automatically control the rate at which said RAM is read out to said replay audio input of said device, said RAM being read out to said replay audio input of said device at a replay rate that is different than an incoming rate at which the incoming audio inputs are received to be stored in said RAM;
 - wherein said replay rate is selected so as to gradually eliminate the delay time without substantially impairing audio quality of the replayed audio, thereby automatically returning said device to normal mode;

wherein when said device is receiving inputs from said RAM, the circuit is in replay mode, and including an output element providing a selected indication that said circuit is in said replay mode, said output element also providing an indication of said delay; and

wherein said output element is a selected display, and wherein said control causes said display to blink at a rate which is a function of said delay.

6. (Original) A circuit as claimed in claim 1, wherein when said device is receiving inputs from said RAM, the circuit is in replay mode, and including a multicolor LED, the LED displaying one color for replay mode, and a second different color for normal mode with incoming audio inputs applied to the device.

7. (Original) A circuit as claimed in claim 6, wherein said RAM is a wrap-around memory, the oldest audio input therein being written over when a new audio input is received and said RAM is full, and wherein said control inhibits writing over of audio inputs in said RAM in response to a selected input from said input component, the circuit being in storage mode when this occurs, and wherein said LED displays a third color when said circuit is in storage mode.

8. (Previously presented) A circuit as claimed in claim 36, wherein said RAM is a wrap-around memory, the oldest audio input therein being written over when a new audio input is received and said RAM is full, and wherein said control inhibits writing over of audio inputs in said RAM in response to a selected input from said input component, the circuit being in storage mode when this occurs, and wherein said control causes incoming audio inputs to be applied to said device when the circuit is in storage mode.

9. (Original) A circuit as claimed in claim 8, wherein said control is operative when the circuit is in storage mode to cause at least selected portions of audio inputs stored in said RAM to be reproduced on said device in response to a selected input from said input component.

10. (Original) A circuit as claimed in claim 9, wherein said selected input is said input component being manually operated for a selected time interval.
11. (Previously presented) A circuit as claimed in claim 1, wherein said device is a radio, and wherein said circuit is returned from replay mode to normal mode with incoming audio inputs applied to said device when there is a station change on said radio.
12. (Original) A circuit as claimed in claim 1, wherein said control processes audio inputs applied to said RAM.
13. (Previously presented) A circuit as claimed in claim 36, wherein said component is operable to indicate a desired rate at which audio inputs are to be reproduced to said device.
14. (Original) A circuit as claimed in claim 13, wherein said component is operable in at least two different ways, said component being operated in a selected way to indicate a desired rate.
15. (Previously presented) A circuit as claimed in claim 36, wherein said control is operative in response to a selected input to set said circuit into an elimination mode, said control being operative when in elimination mode to store in said RAM a selected duration of audio inputs ahead of inputs received by said RAM, and is responsive, when in elimination mode, to a selected input from said component for skipping an audio duration in said RAM which is less than said selected duration, whereby audio during said audio duration is not reproduced at said device.
16. (Original) A circuit as claimed in claim 15, wherein said audio duration is variable in response to variations in the selected input from said component.

17. (Original) A circuit as claimed in claim 15, wherein said control is operative when in elimination mode to store said selected duration in said RAM before applying audio inputs from said RAM to said device.

18. (Original) A circuit as claimed in claim 15, wherein said control is operative, when in elimination mode to apply audio inputs to said device from said RAM, said RAM being read out to apply inputs to said device at a slower rate than audio inputs are received to be stored in said RAM at any time said RAM is not storing at least said selected duration of audio inputs.

19. Canceled.

20. Canceled.

21. Canceled.

22. Canceled.

23. Canceled.

24. Canceled.

25. Canceled.

26. Canceled

27. Canceled.

28. (Previously presented) A repeat circuit for use with a radio having a normal mode and a replay mode, the repeat circuit including:

a RAM connected to normally receive and store audio inputs applied to said radio; and

a control adapted to switch said radio from normal mode into replay mode by inhibiting application of incoming audio inputs to said radio and for instead applying audio inputs stored in said RAM as audio inputs to said radio, the audio reproduced by said device being selectively delayed from the incoming audio inputs by a time period;

wherein said radio is automatically returned from replay mode to normal mode with incoming audio inputs applied to said radio when there is a station change on said radio.

29. (Previously presented) The repeat circuit as claimed in claim 28, wherein said control is adapted to control the rate at which said RAM is read out to apply audio inputs to said device; the rate at which said RAM is read out being faster than the input rate at which said audio inputs are received to be stored in said RAM.

30. (Previously presented) The repeat circuit as claimed in claim 28, wherein said control is adapted to control the rate at which said RAM is read out to apply audio inputs to said device; the rate at which said RAM is read out being slower than the input rate at which said audio inputs are received to be stored in said RAM.

31. Cancelled.

32. Cancelled.

33. Cancelled.

34. Cancelled.

35. Cancelled.

36. (Previously presented) The repeat circuit as claimed in claim 1, further comprising a manually operable input component, and wherein said control is operable in response to a

selected input from said input component, to switch said audio receive and reproduce device into replay mode.

37. (Previously presented) The repeat circuit as claimed in claim 28, further comprising a manually operable input component; wherein said control is operable in response to a selected input from said input component, to switch said radio from normal mode into replay mode.

38 (Previously presented) The repeat circuit as claimed in claim 37, wherein said control is operable in response to a rate indication from said input component for controlling the rate at which said RAM is read out to apply audio inputs.

39 (Previously presented) The repeat circuit as claimed in claim 38, wherein the rate at which said RAM is read out is different than an input rate at which said audio inputs are received to be stored.

40. Canceled.